

## CLAMP

Abstract

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This invention relates to a device for protecting a circuit against voltage surges, including a MOS transistor of a first type connected to first and second supply terminals by its source and its drain, respectively; a MOS transistor of a second type connected between the second supply terminal and the gate of the first type transistor, by its source and its drain, respectively; and a capacitor having a first terminal connected to the first supply terminal and a second terminal connected to the gate of the second type transistor.